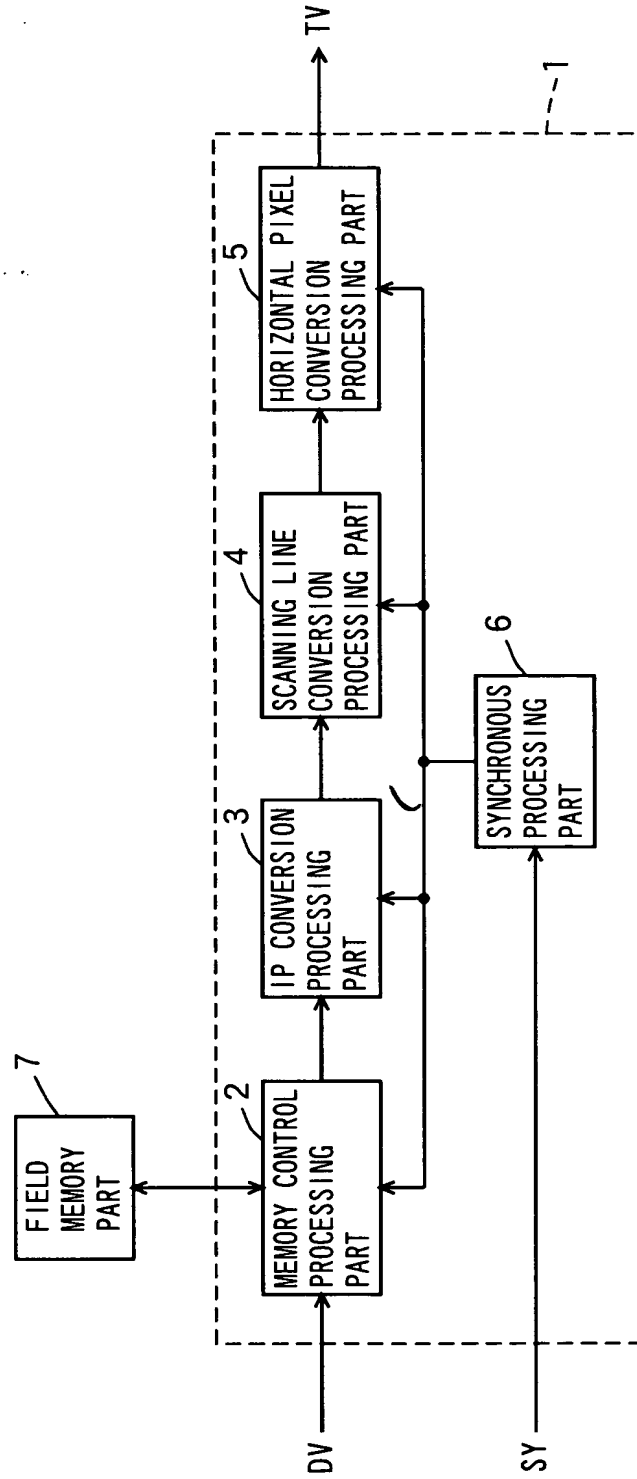


FIG. 1



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FIG. 2

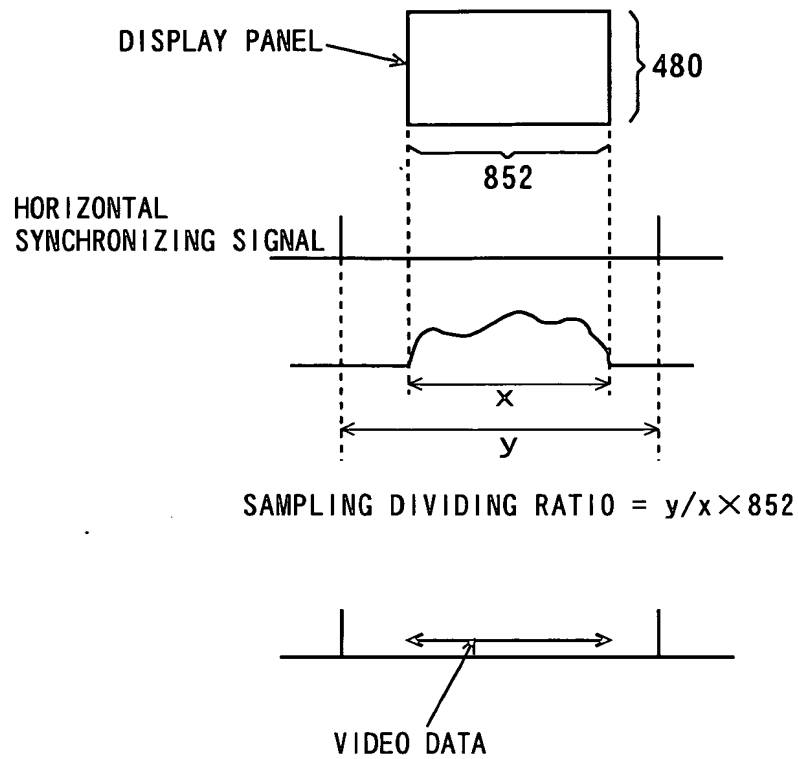
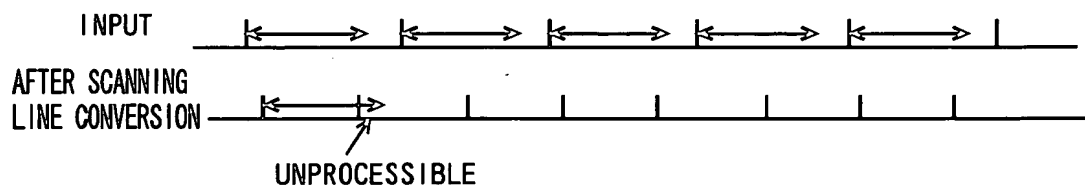
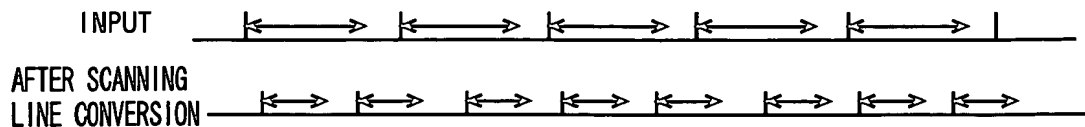


FIG. 3

(a) CASE OF CLOCK IDENTICAL TO INPUT



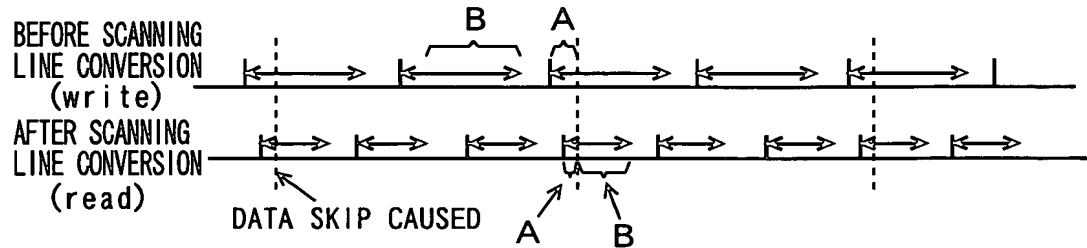
(b) CASE OF CLOCK SUFFICIENTLY QUICK AS COMPARED WITH INPUT



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F I G. 4

(a) WHEN TRANSFERRING CLOCK BEFORE AND AFTER SCANNING LINE CONVERSION



(b) WHEN TRANSFERRING CLOCK AFTER SCANNING LINE CONVERSION

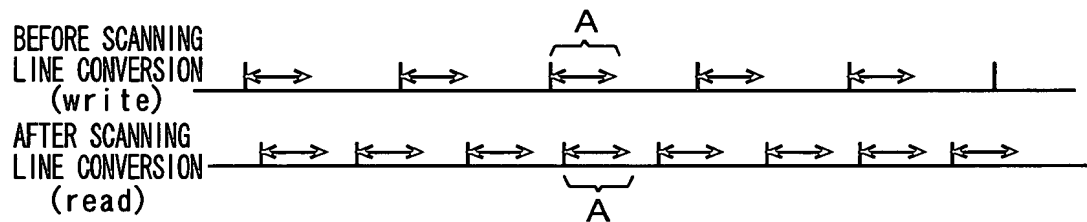
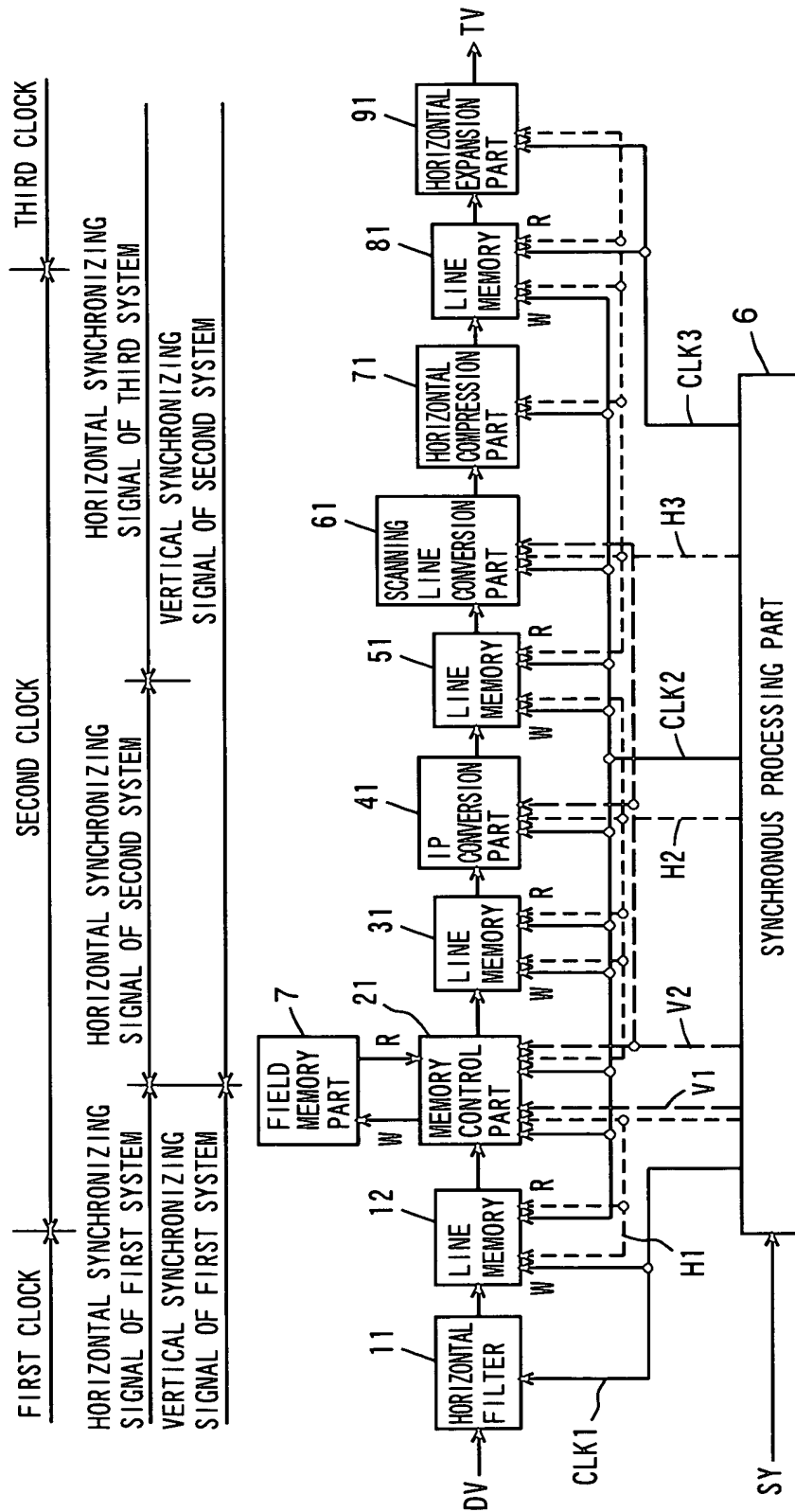


FIG. 5



F I G. 6

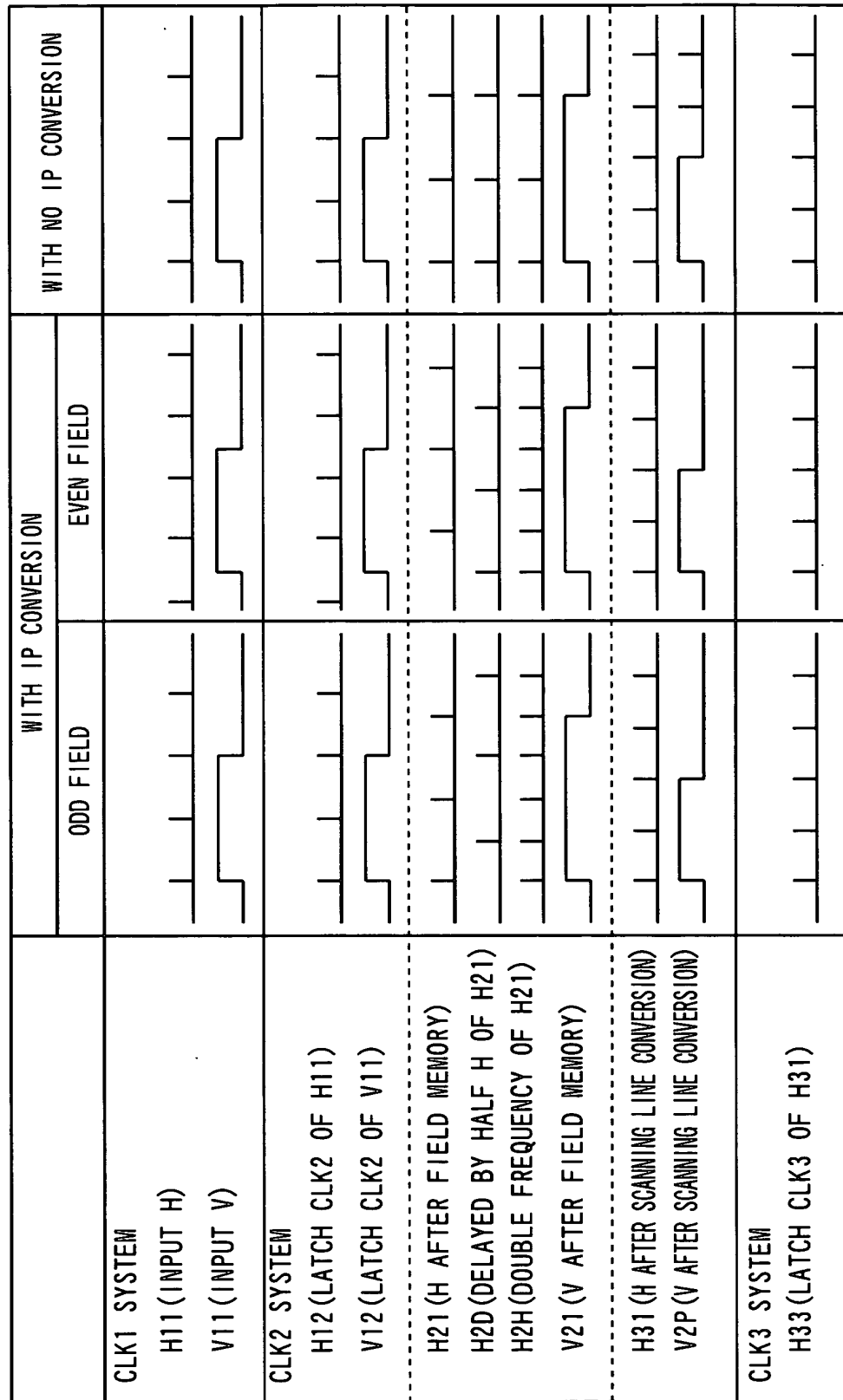


FIG. 7

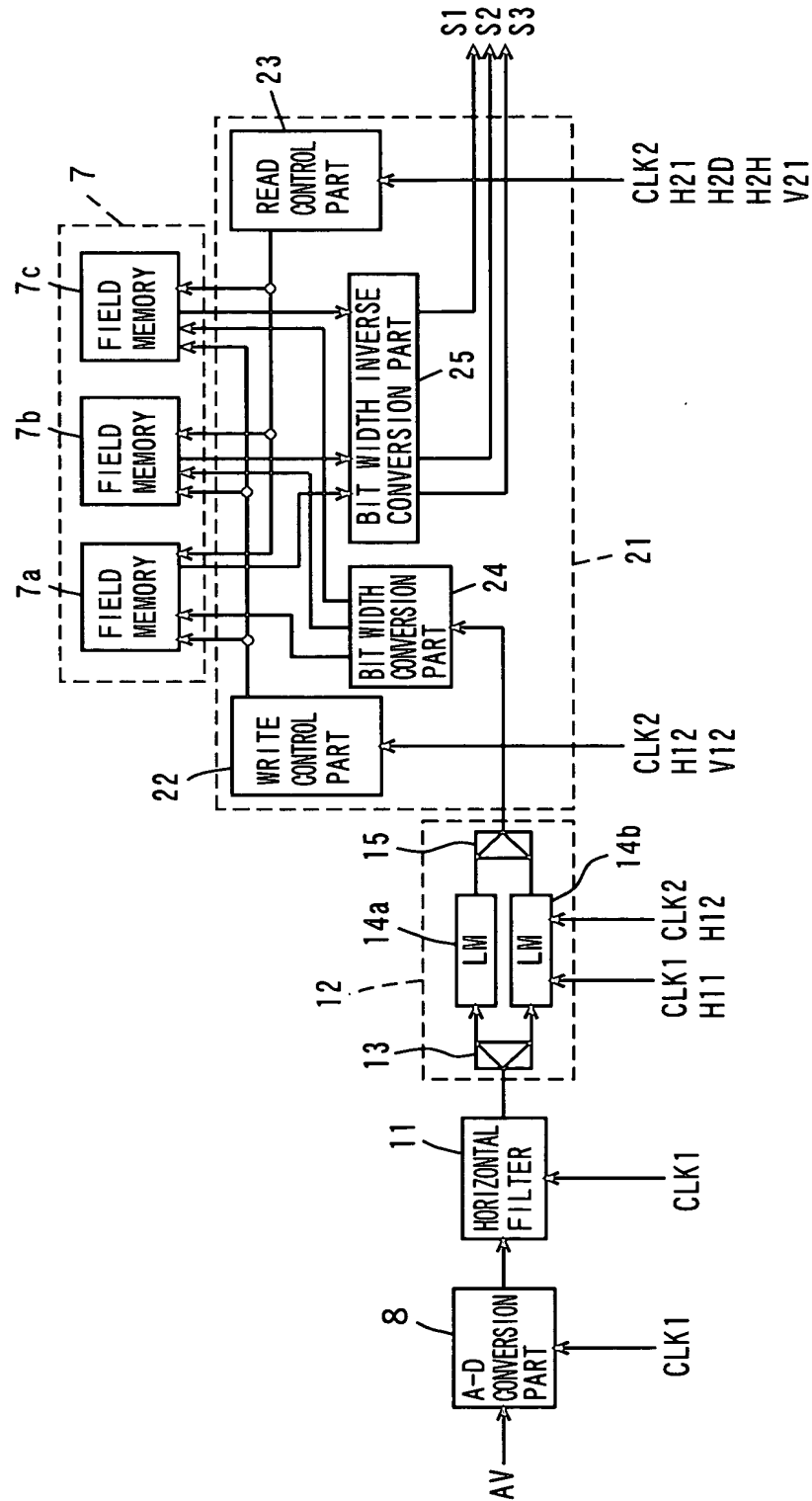


FIG. 8

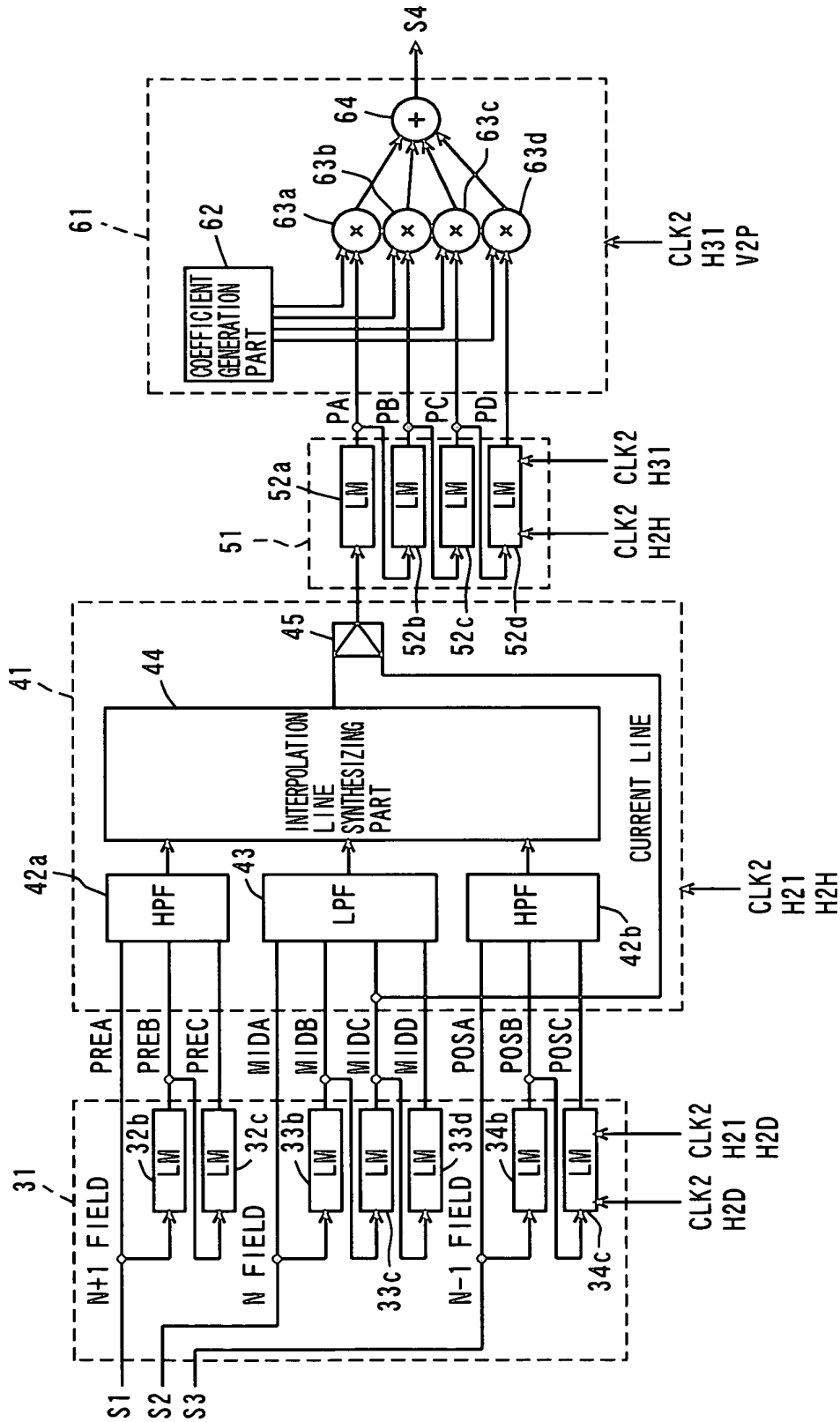


FIG. 9

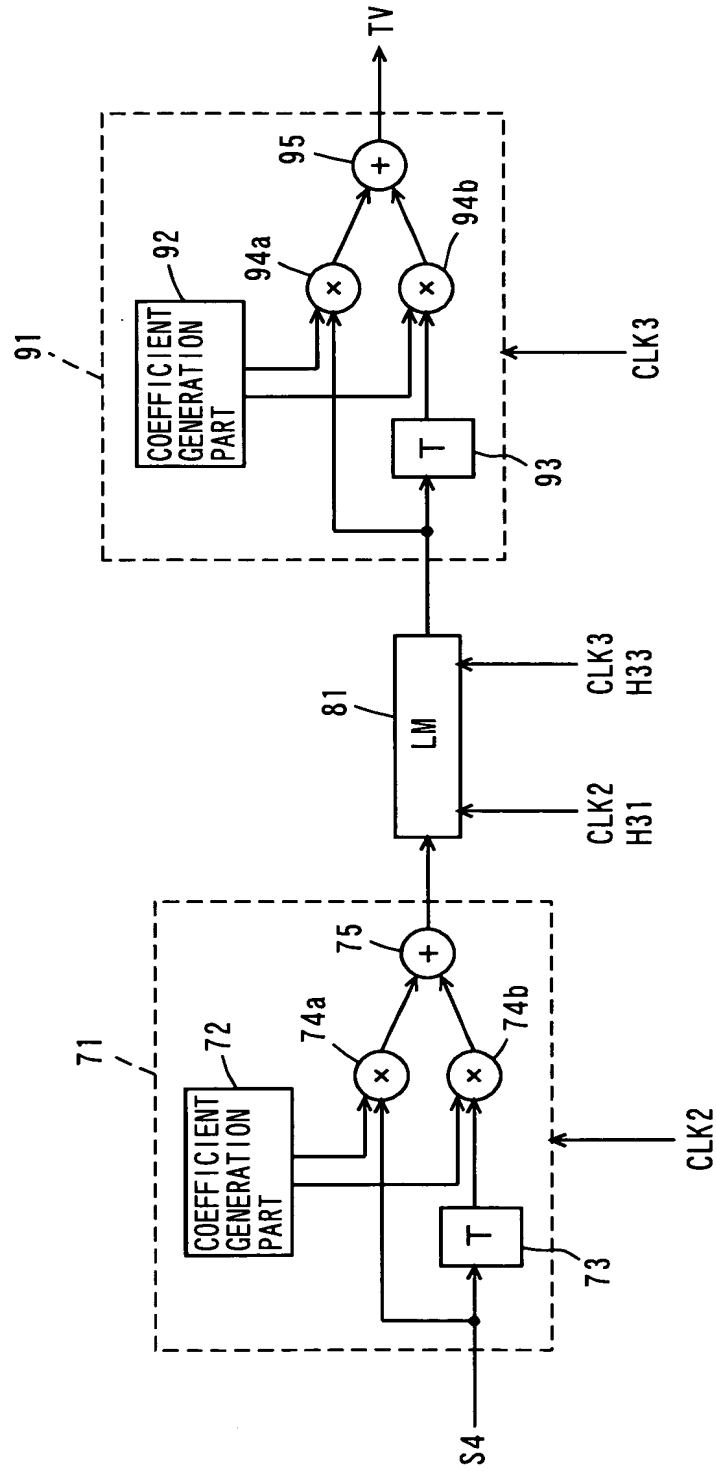


FIG. 10

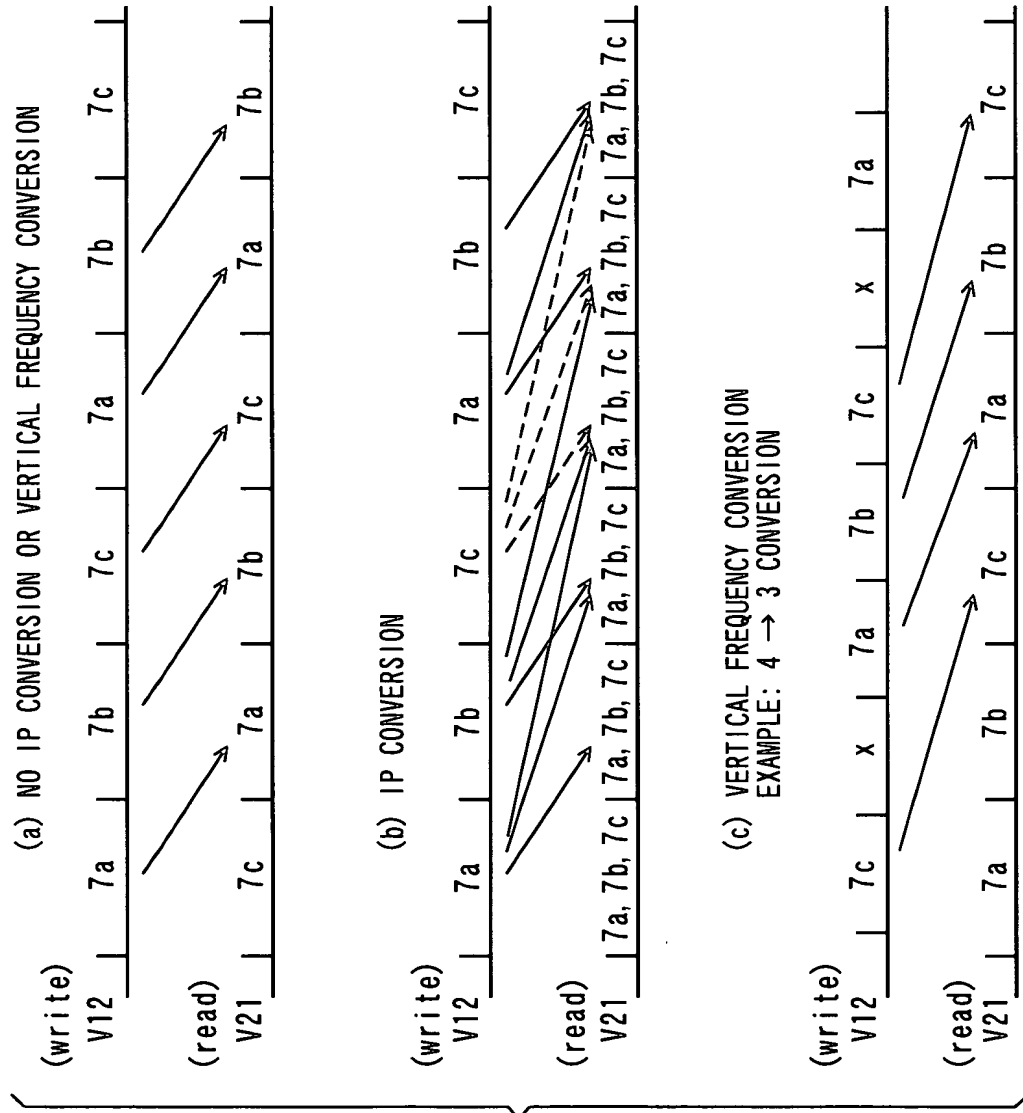
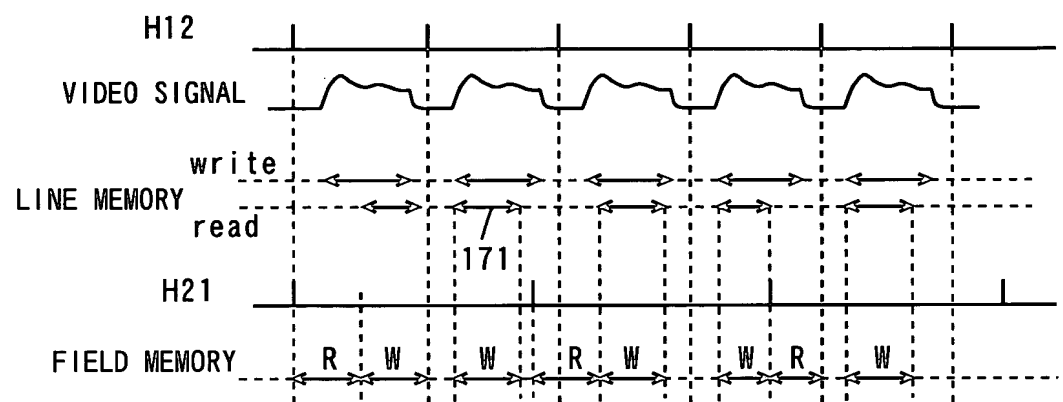


FIG. 11

(a) WITH ONE LINE MEMORY



(b) WITH TWO LINE MEMORIES

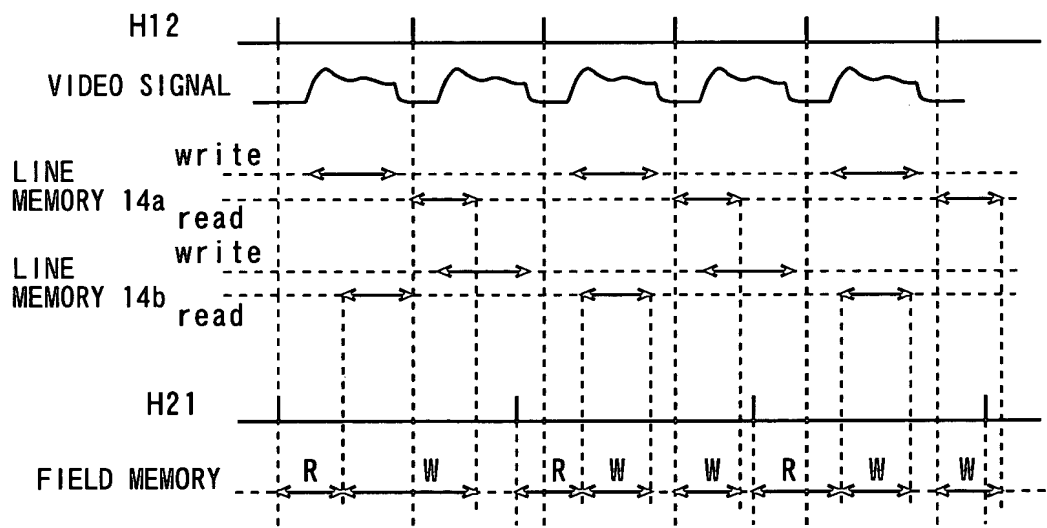
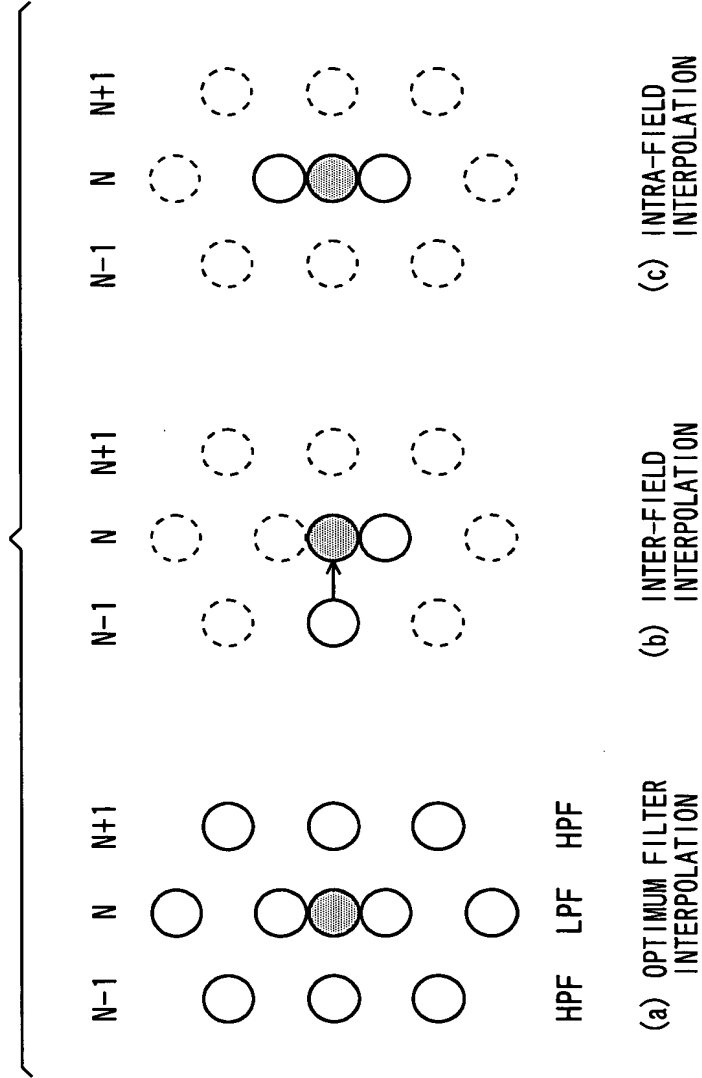


FIG. 12



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FIG. 13

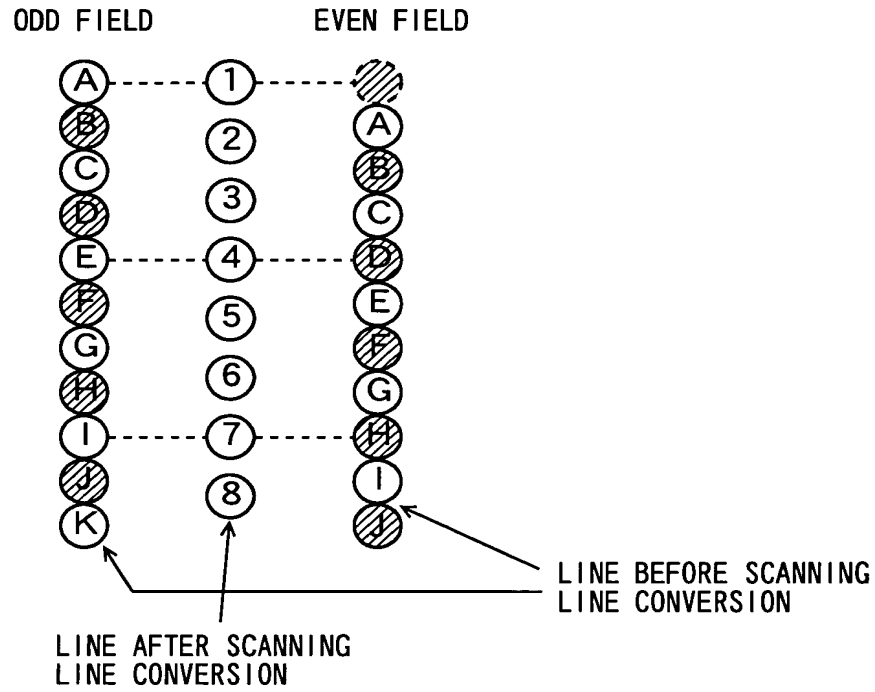


FIG. 14

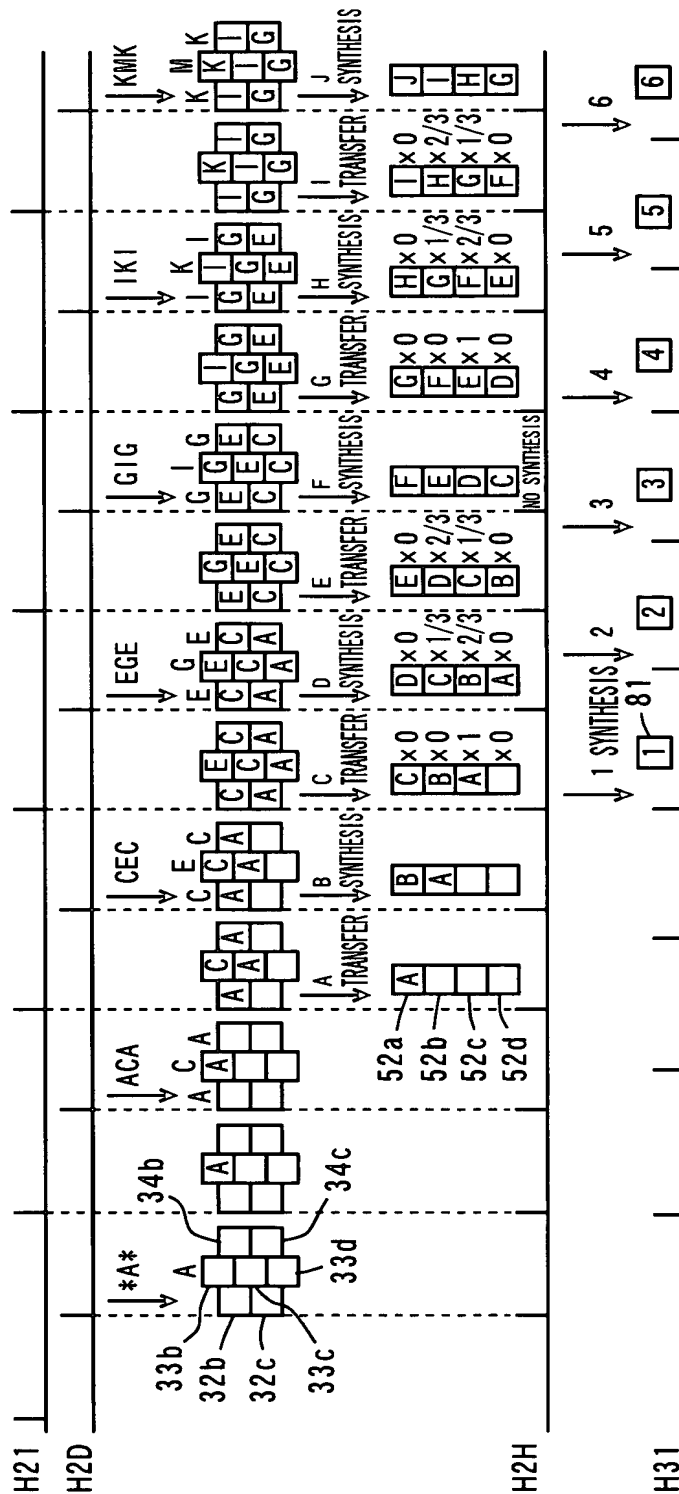


FIG. 15

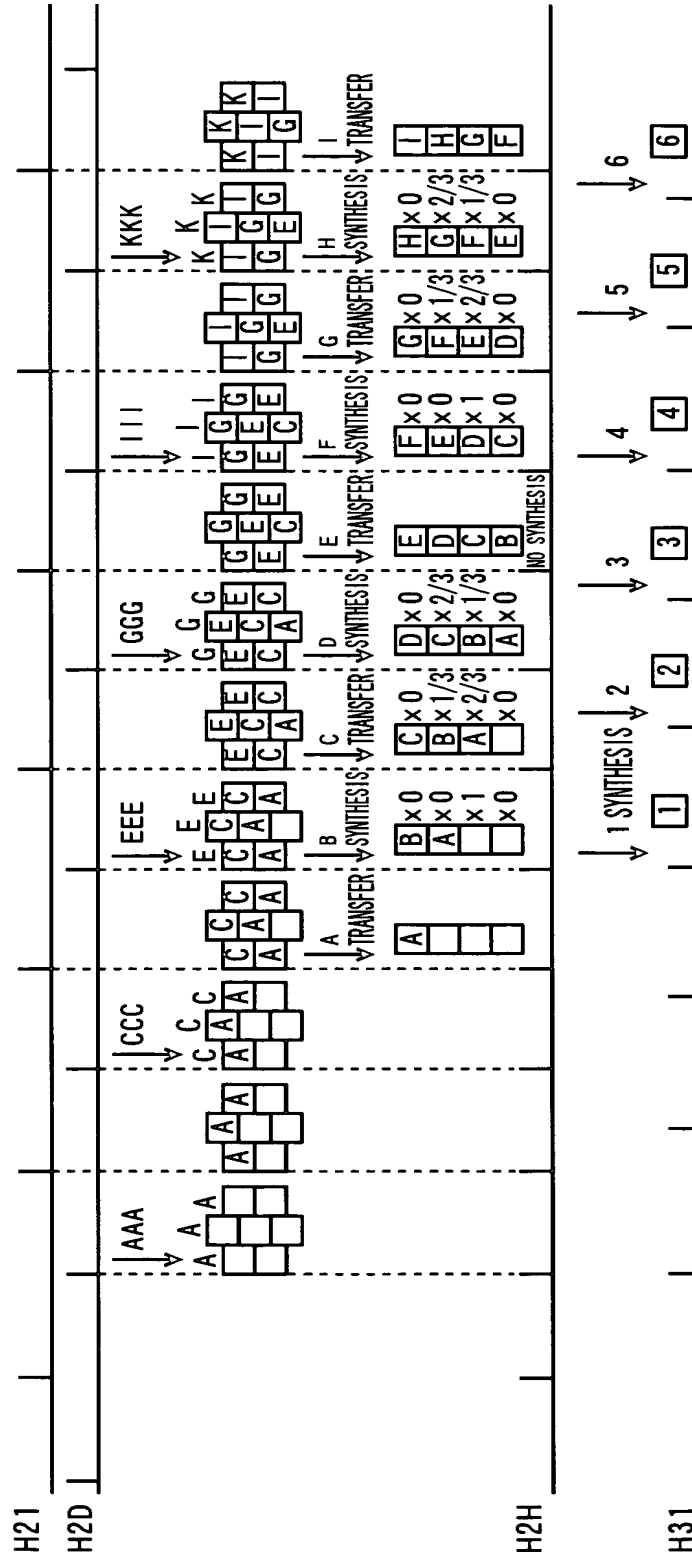


FIG. 16

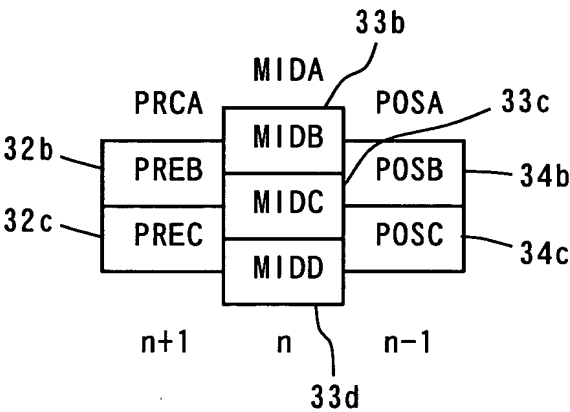


FIG. 17

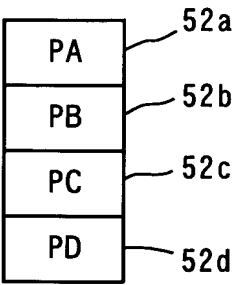
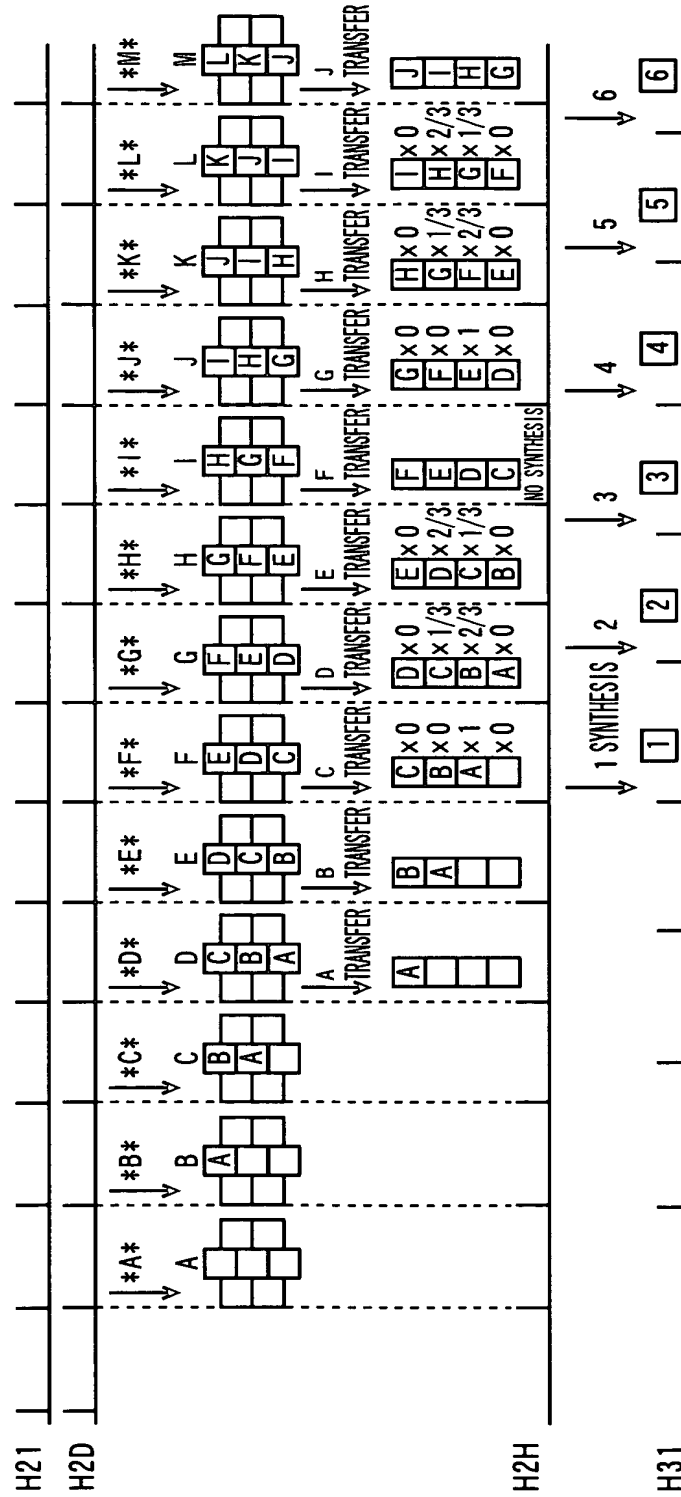
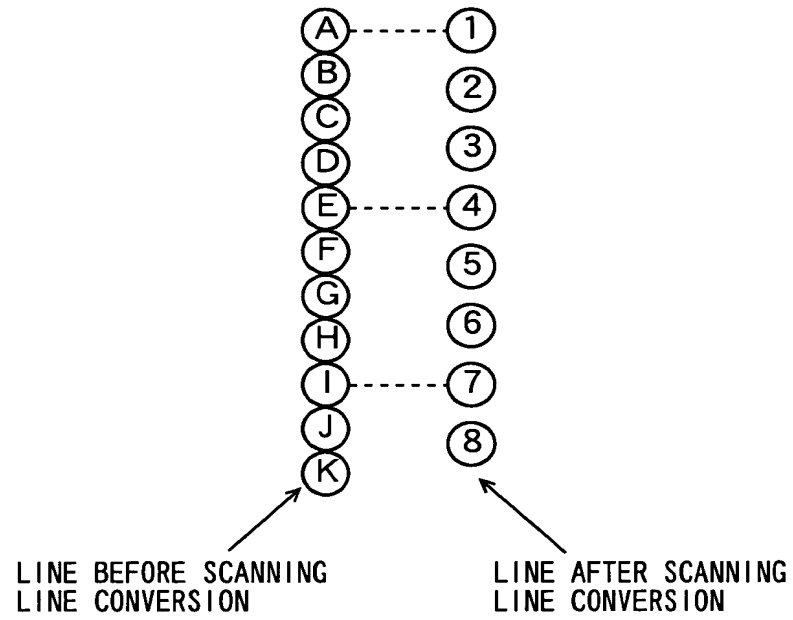


FIG. 18



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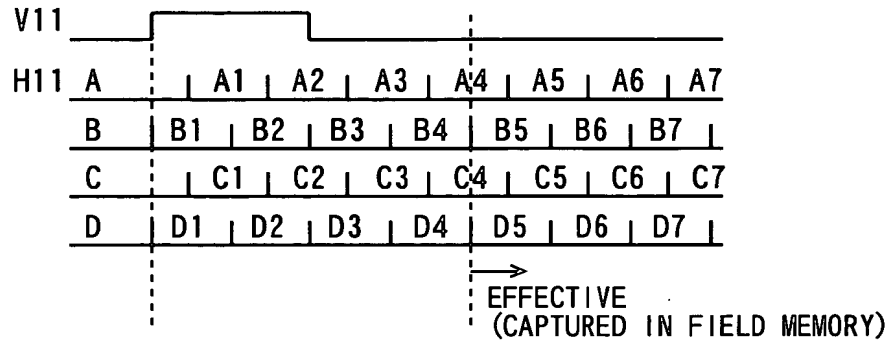
FIG. 19



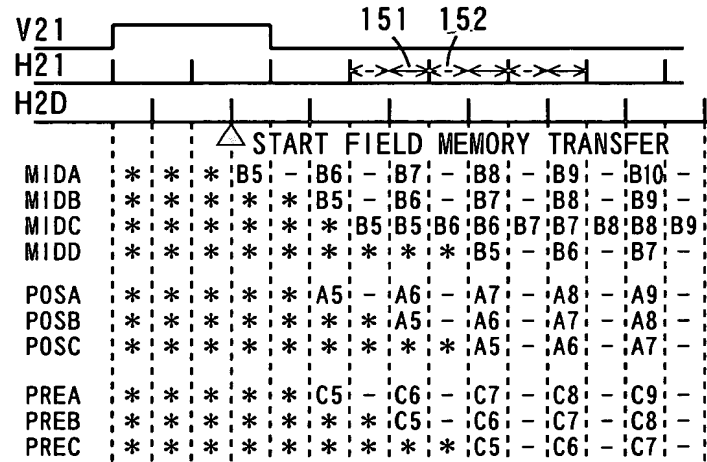
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FIG. 20

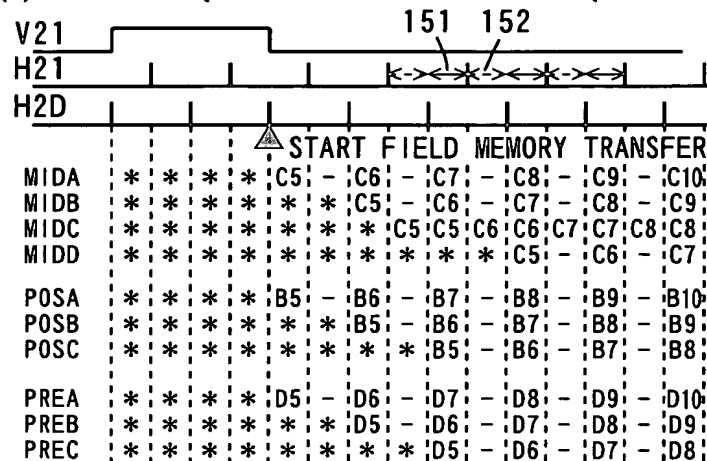
(a) INPUT



(b) OUTPUT SEQUENCE FROM FIELD MEMORY (ODD FIELD)



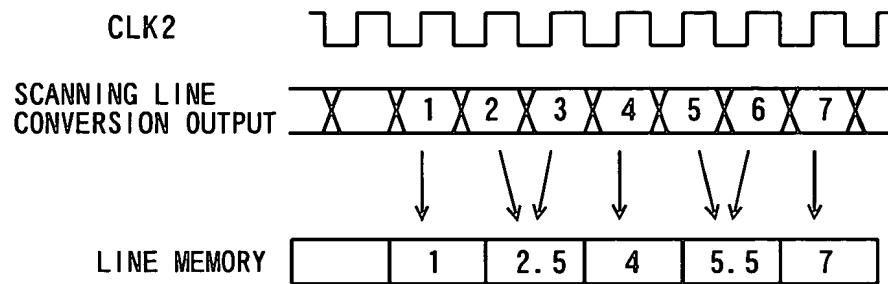
(c) OUTPUT SEQUENCE FROM FIELD MEMORY (EVEN FIELD)



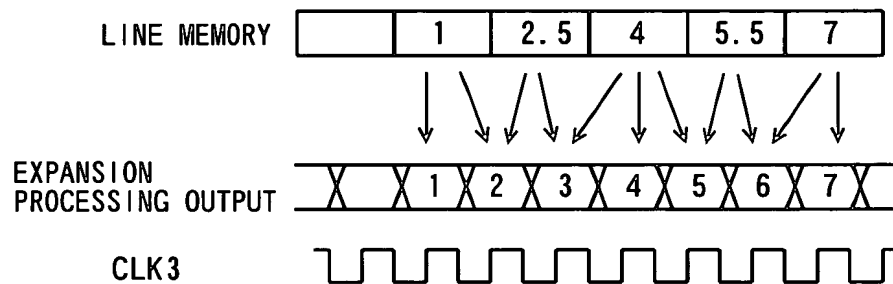
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FIG. 21

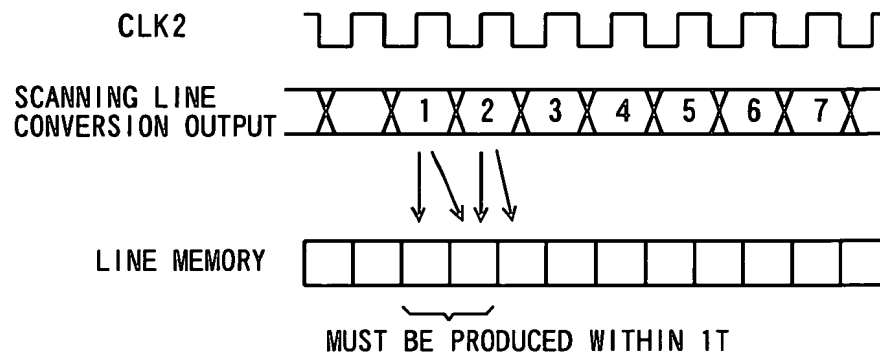
(a) WRITING (3 → 2 CONVERSION)



(b) READING (2 → 3 CONVERSION)



(c) WRITING COMBINED WITH EXPANSION PROCESSING (3 → 4 CONVERSION)



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FIG. 22

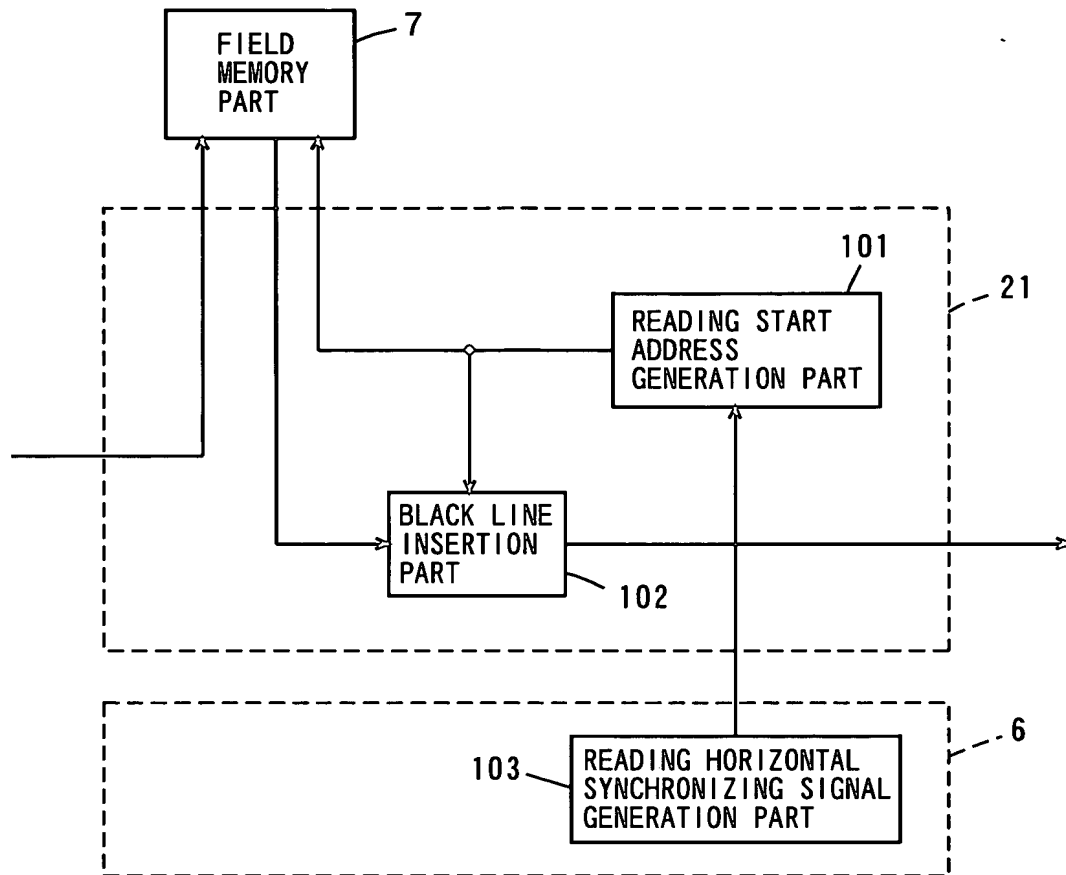
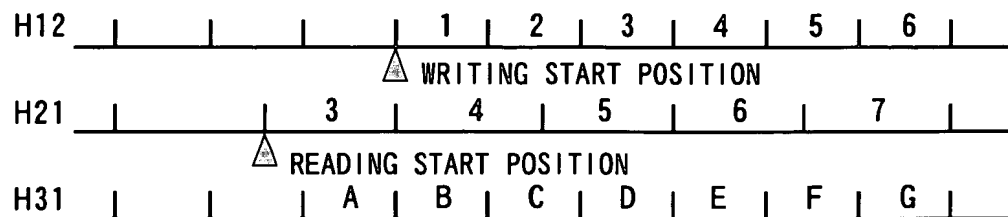


FIG. 23



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FIG. 24

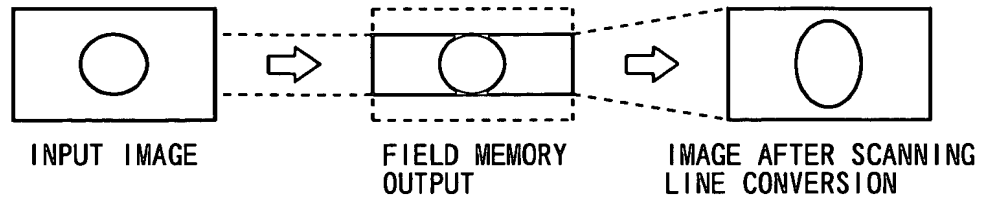


FIG. 25

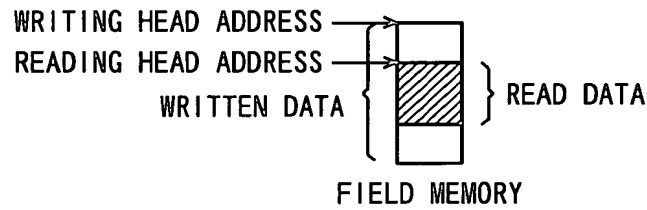
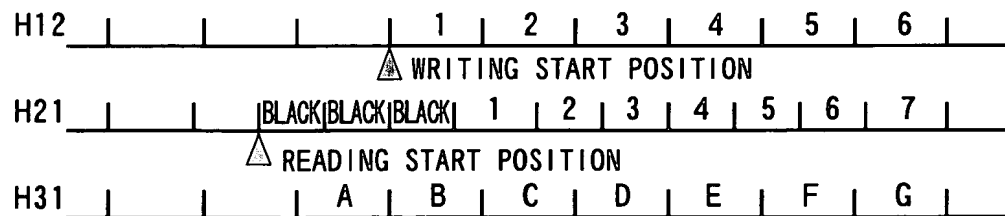


FIG. 26



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FIG. 27

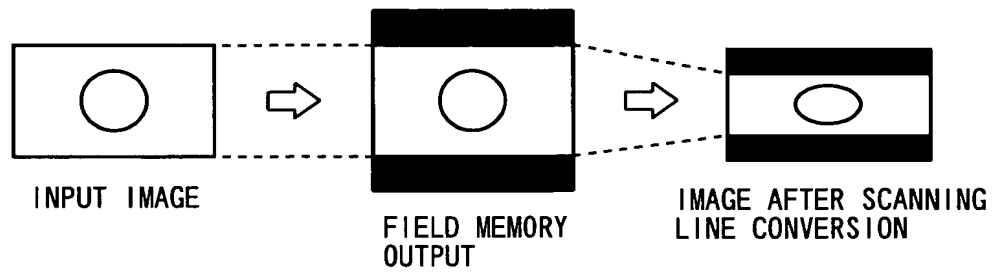


FIG. 28

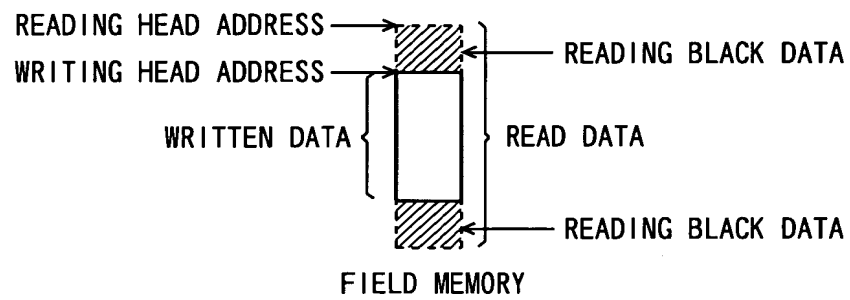


FIG. 29

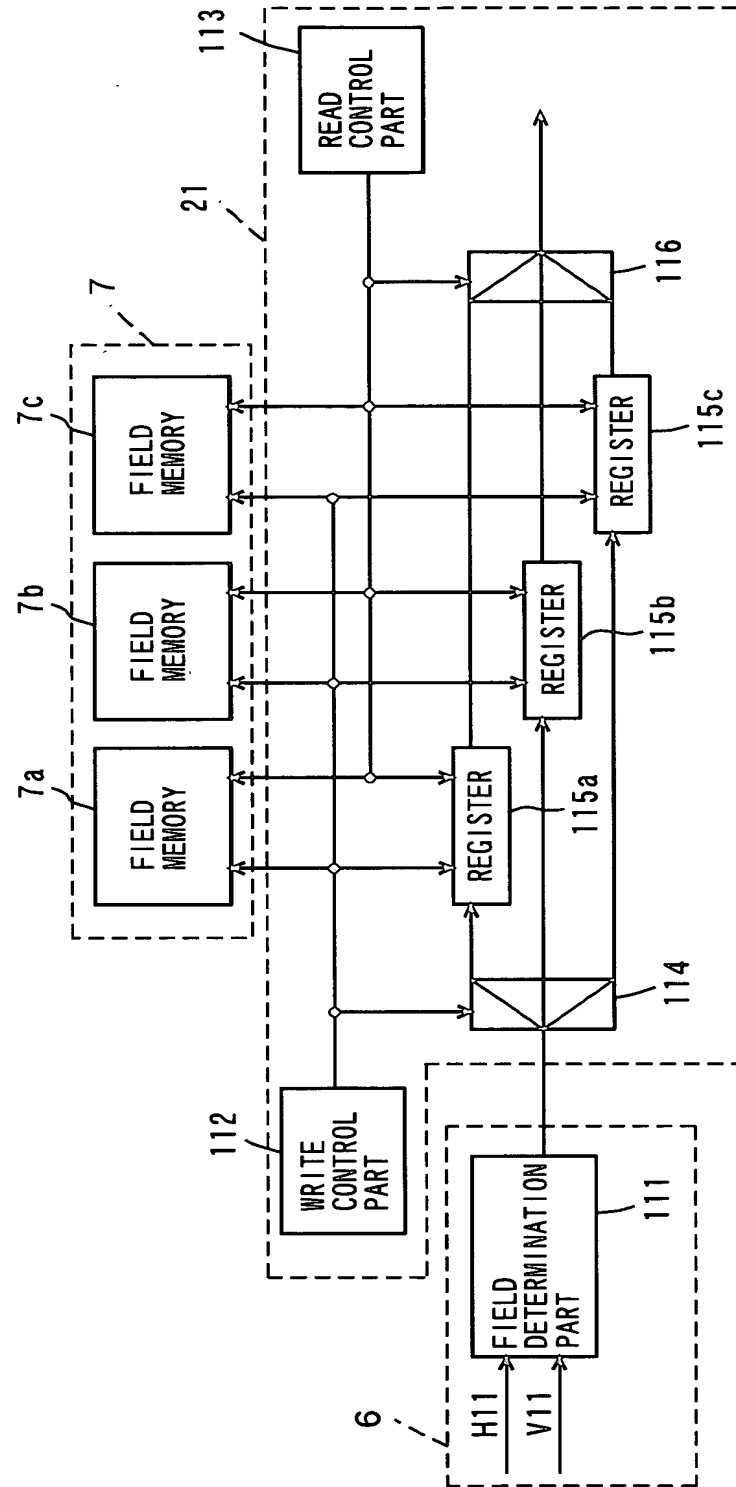


FIG. 30

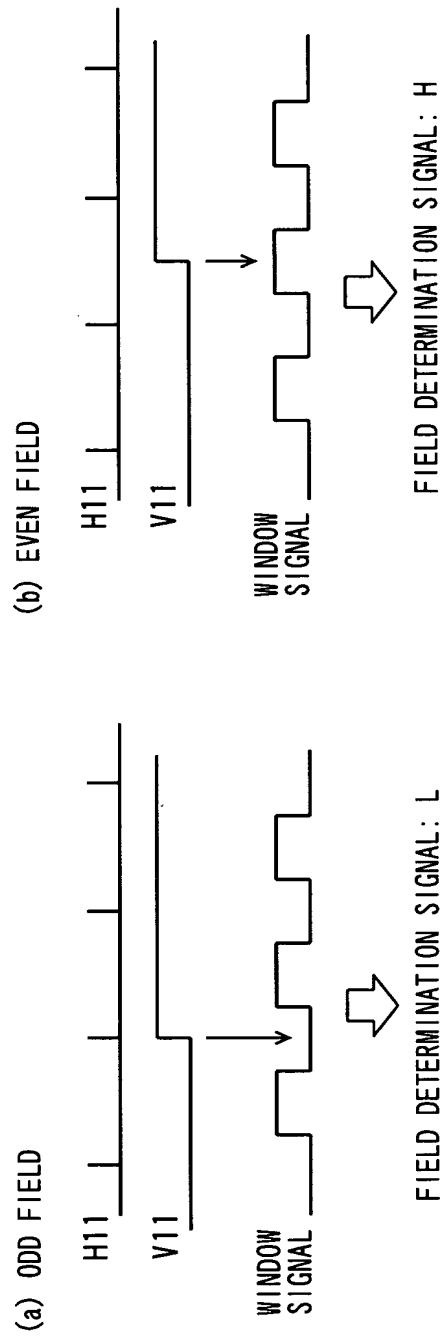


FIG. 31

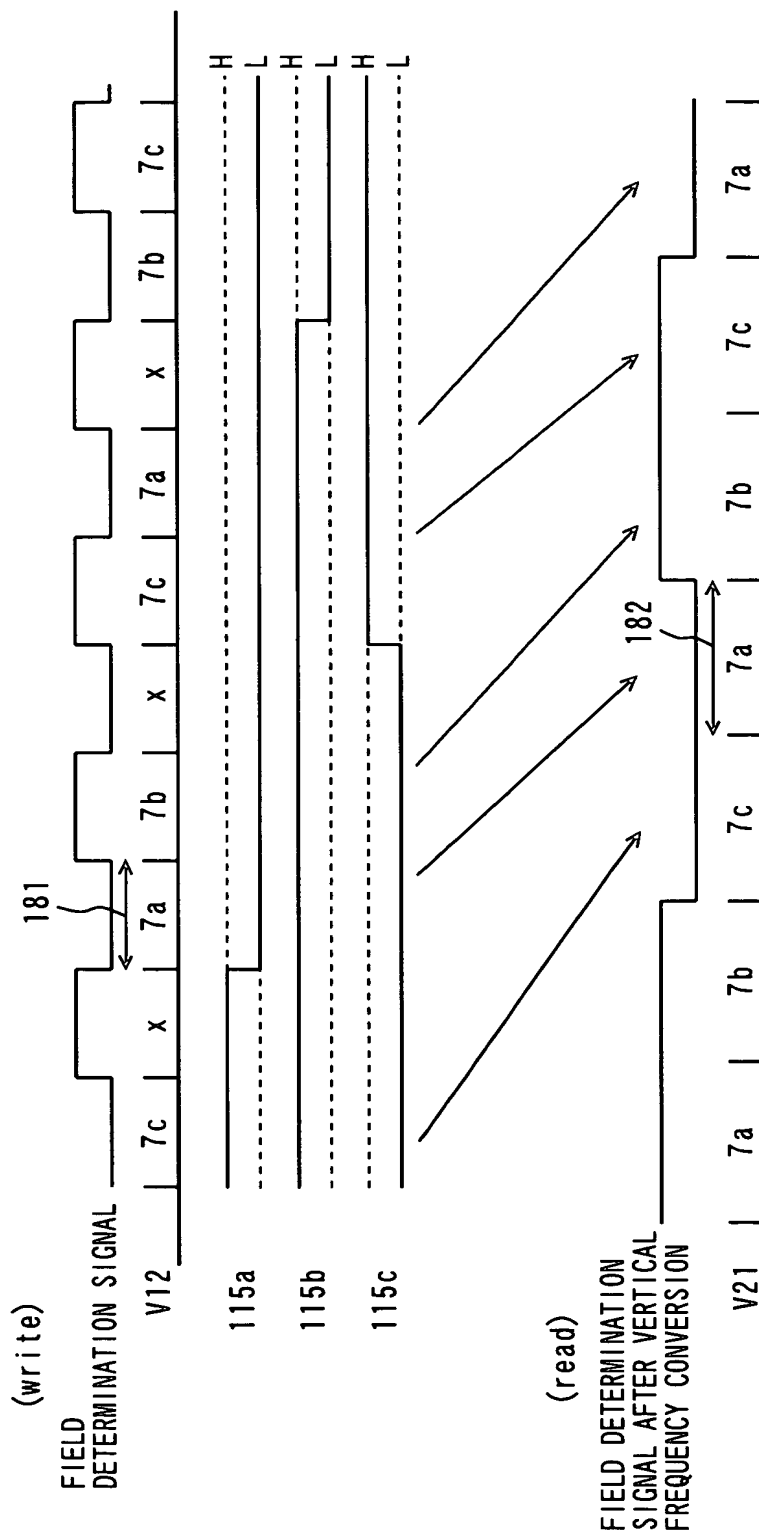


FIG. 32

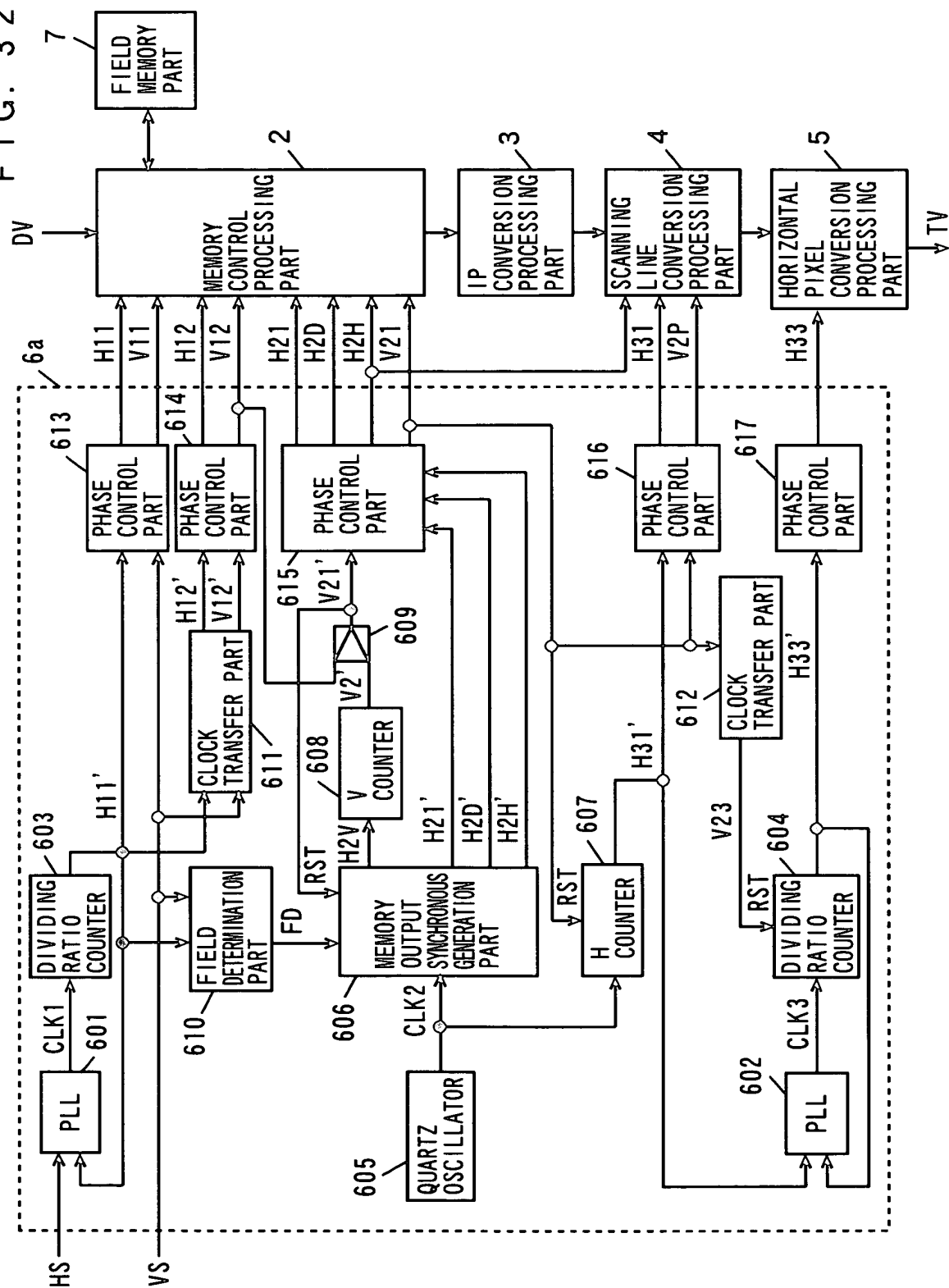
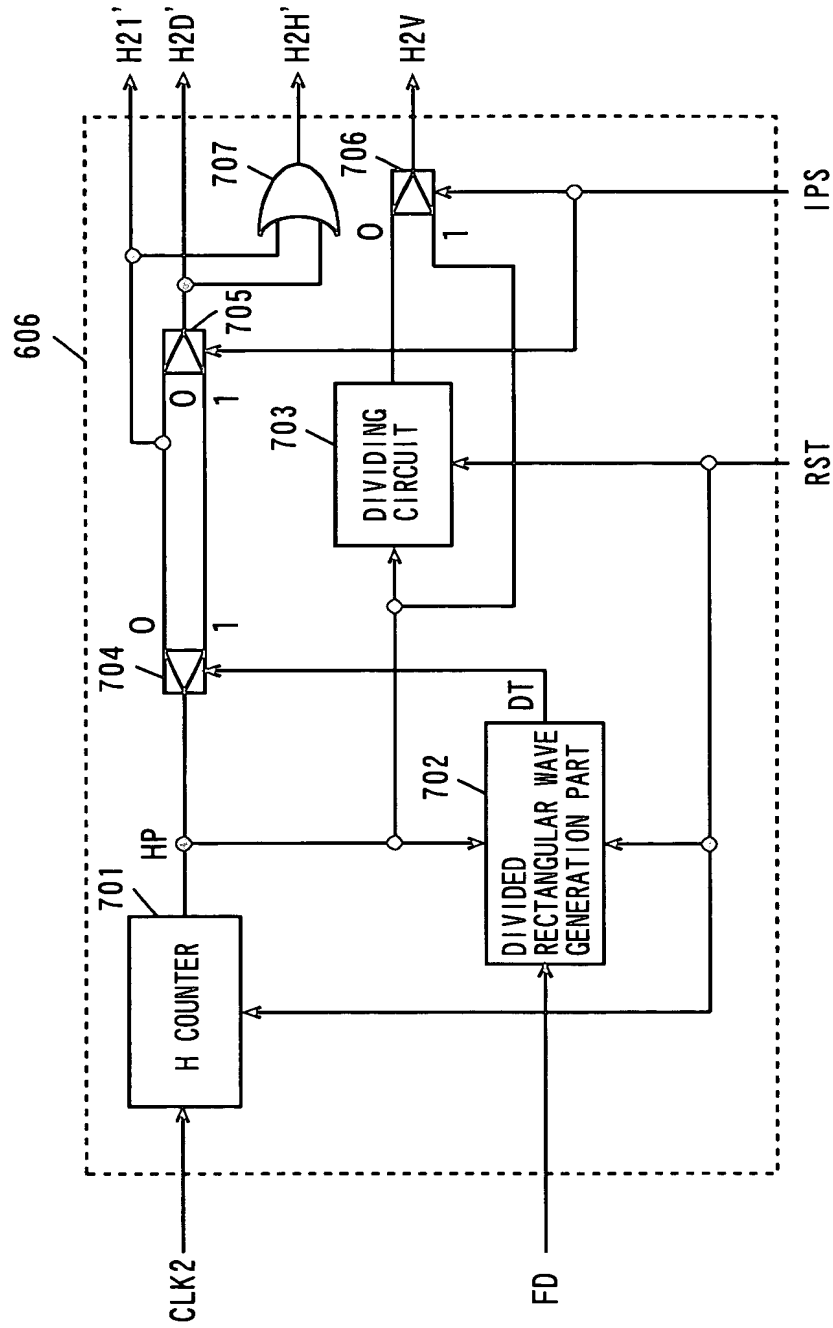


FIG. 33



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FIG. 34

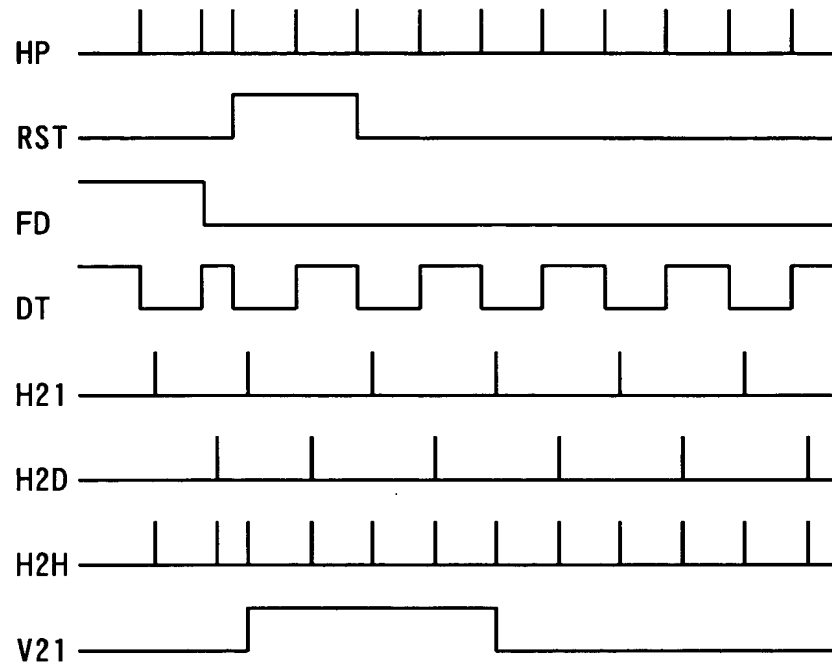
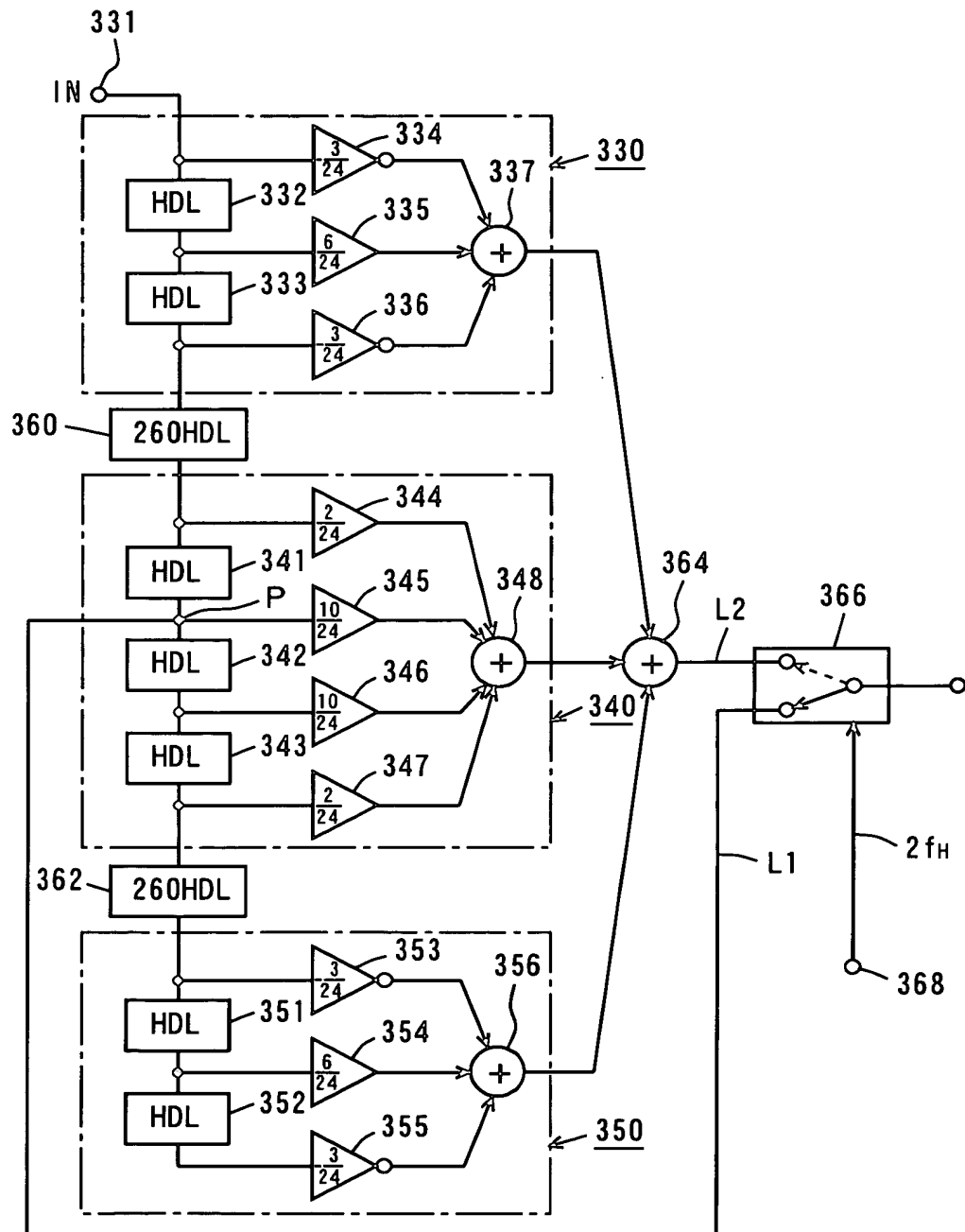


FIG. 35



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FIG. 36

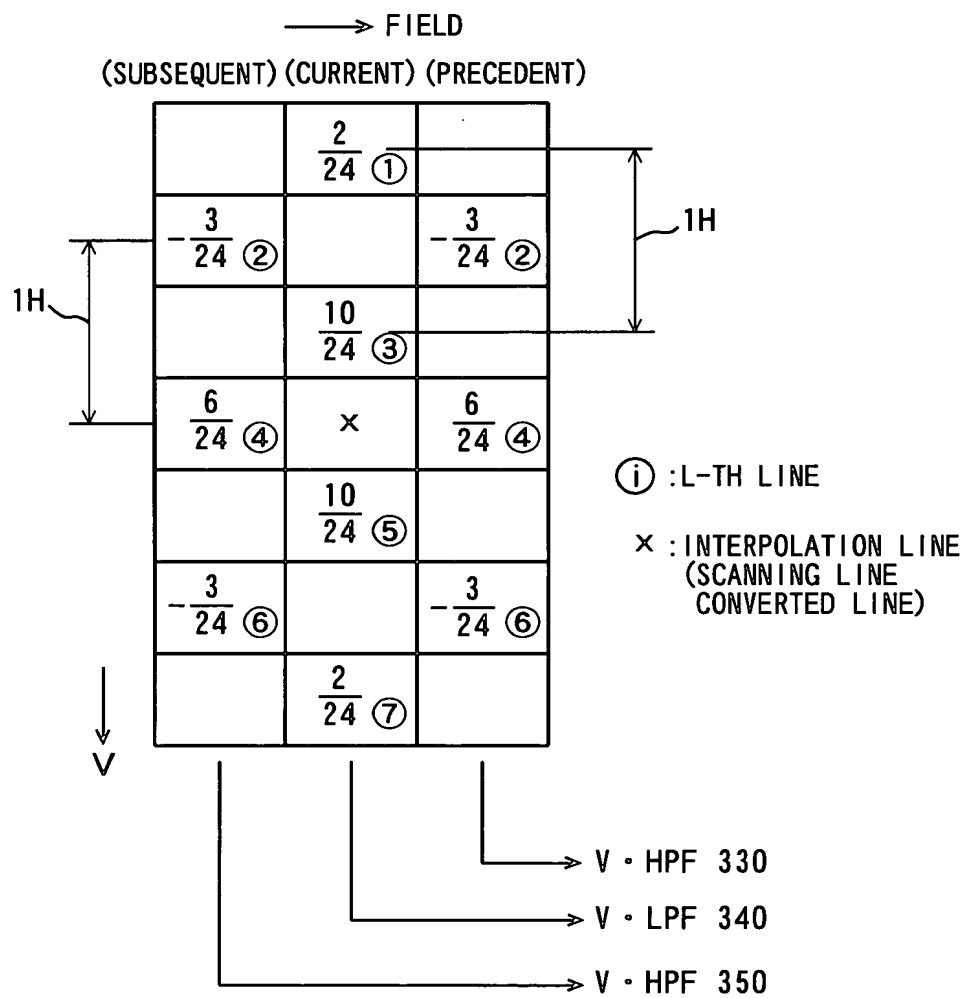


FIG. 37

